

5 Instructions are processed by a central processing unit (CPU) in a computer.

(a) Complete the paragraph about fetching an instruction into the CPU to be processed.

Use the terms from the list.

Some of the terms in the list will **not** be used. You should only use a term once.

- address
- arithmetic logic unit (ALU)
- binary
- control unit (CU)
- current instruction register (CIR)
- data
- denary
- driver
- fetch
- interrupt
- memory address register (MAR)
- memory data register (MDR)
- random access memory (RAM)
- read only memory (ROM)
- secondary storage
- signal

The program counter contains the
of the next instruction to be processed; this is then sent to the
..... using the address bus. The address is then
sent to the
Once the address is received, the instruction stored at the location is
sent to the, using the
..... bus. The instruction is then
sent to the that is built into the
.....

[7]

(b) The CPU uses an instruction set to decode the instruction.

State what is meant by an instruction set.

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..... [1]