

3 A computer with a Von Neumann architecture has several hardware components.

(a) Complete the table with the missing terms and descriptions about computer hardware.

Term	Description
.....	This is the address given to a network interface card (NIC) when it is manufactured.
register
clock
control unit (CU)
.....	This is a processing unit within the CPU that can fetch, decode and execute instructions.

[5]

(b) Identify **three** registers that are used in the fetch stage of the fetch–decode–execute (FDE) cycle.

- 1
- 2
- 3

[3]

(c) Circle the **two** buses that are used in the fetch stage of the FDE cycle.

- binary control denary data
 information hardware software

[2]