

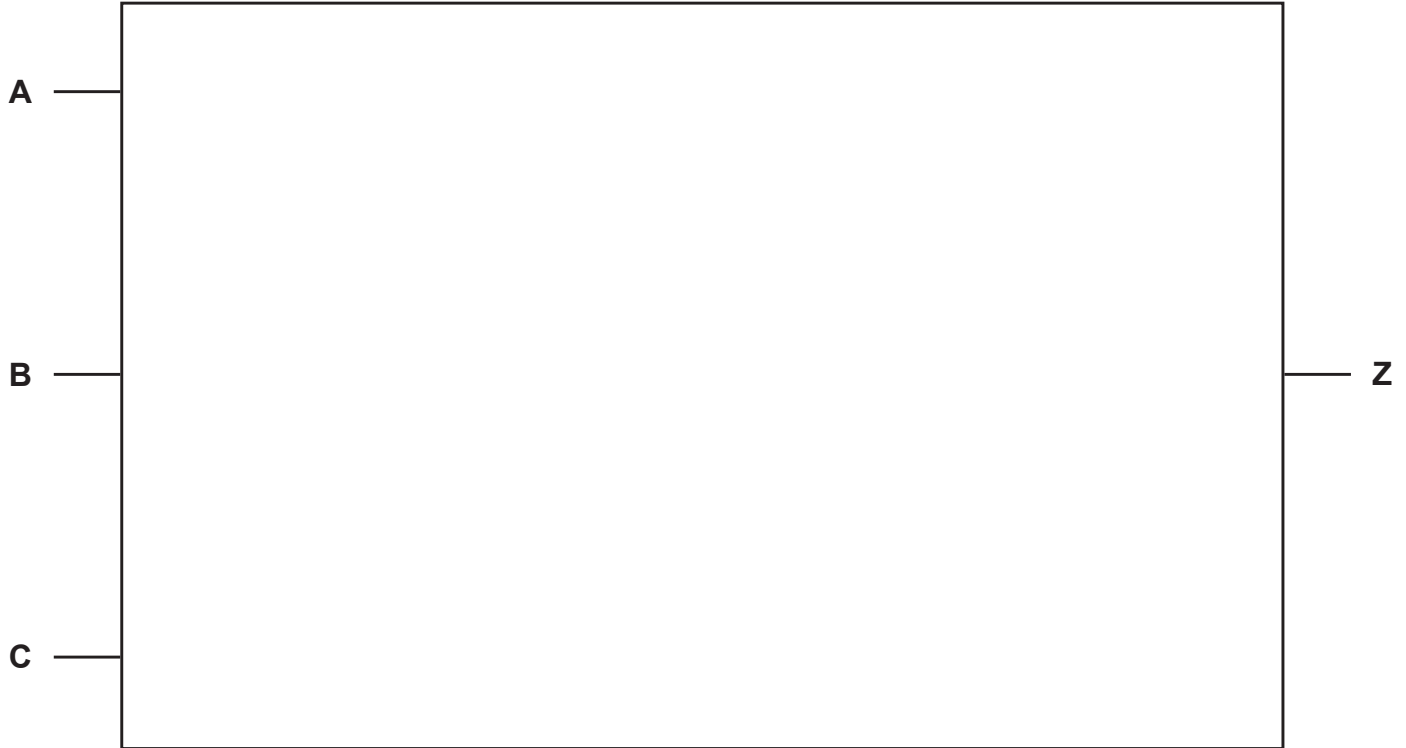
9 Consider the logic expression:

$$Z = (A \text{ NAND } B) \text{ OR NOT } (B \text{ XOR } C)$$

(a) Draw a logic circuit for this logic expression.

Each logic gate must have a maximum of **two** inputs.

Do **not** simplify this logic expression.



[4]

(b) Complete the truth table from the given logic expression.

A	B	C	Working space	Z
0	0	0		
0	0	1		
0	1	0		
0	1	1		
1	0	0		
1	0	1		
1	1	0		
1	1	1		

[4]