

3 An instruction is fetched from random access memory (RAM) into the memory data register (MDR) to be decoded.

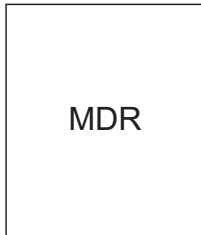
(a) Identify **two** other registers that are used in the fetch stage of the cycle.

1

2

[2]

(b) Complete and annotate the diagram to show how the data is decoded once it has been fetched into the MDR.



[4]